

1C914 U.S. PTO 09/138855 12/28/00	Class	Subclass	ISSUE CLASSIFICATION
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PATENT NUMBER

U.S. UTILITY Patent Application

<p>11km SCANNED</p> <p>O.I.P.E. <u>3</u> K96333 Q.A. <u>10</u></p>	<p>PATENT DATE</p>
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APPLICATION NO. 09/738855	CONT/PRIOR F	CLASS 029	SUBCLASS B52	ART UNIT 3729	EXAMINER chang
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APPLICANTS

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Method of producing a multi-layered wiring board

PTO-2040
12/89

ISSUING CLASSIFICATION

[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets.Drwg.	Figs..Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) _____ (Date)		NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner) _____ (Date)		ISSUE FEE	
			Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) _____ (Date)		ISSUE BATCH NUMBER	

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